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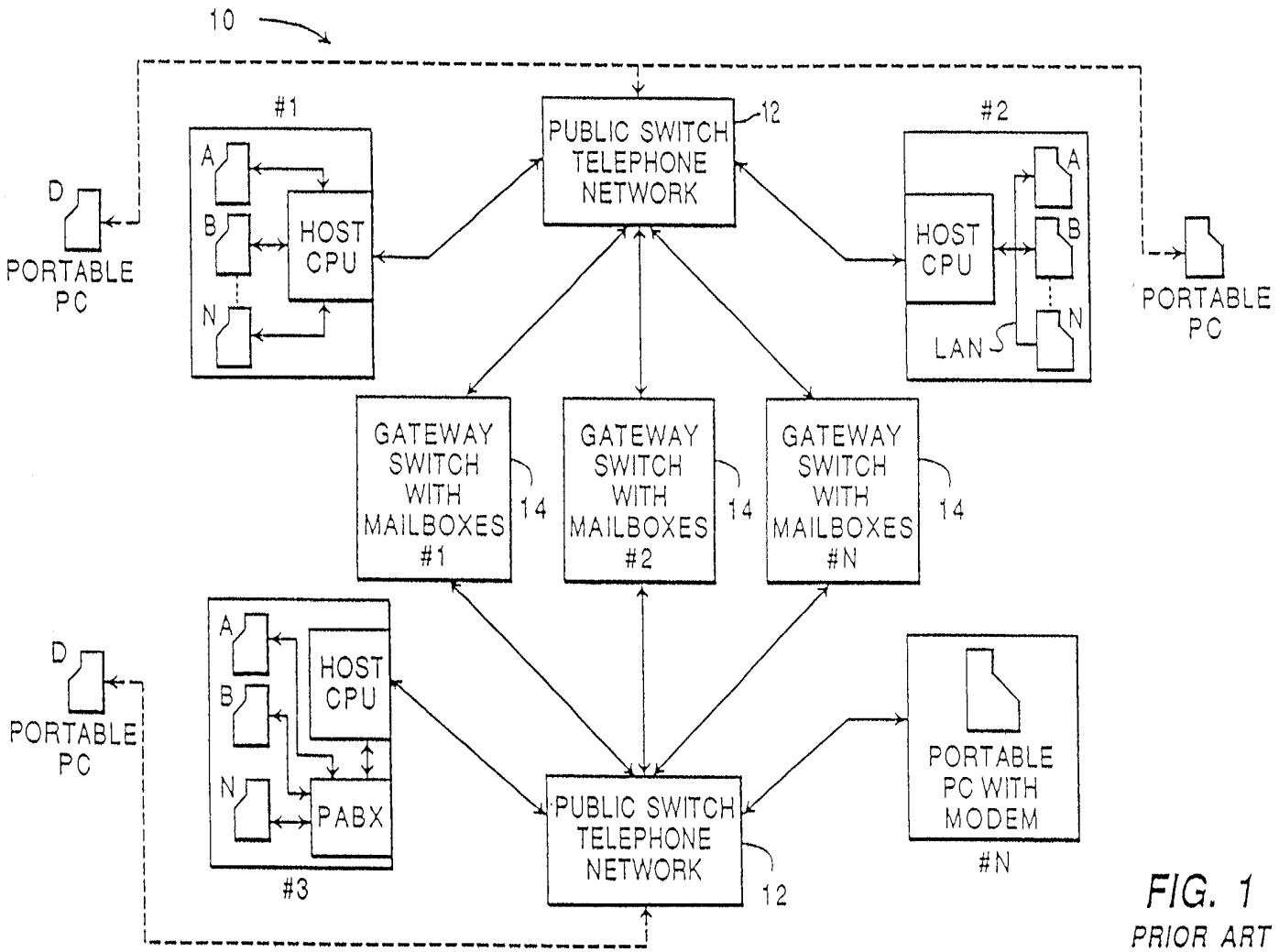


FIG. 1
PRIOR ART

30h0E0 '9h29801T

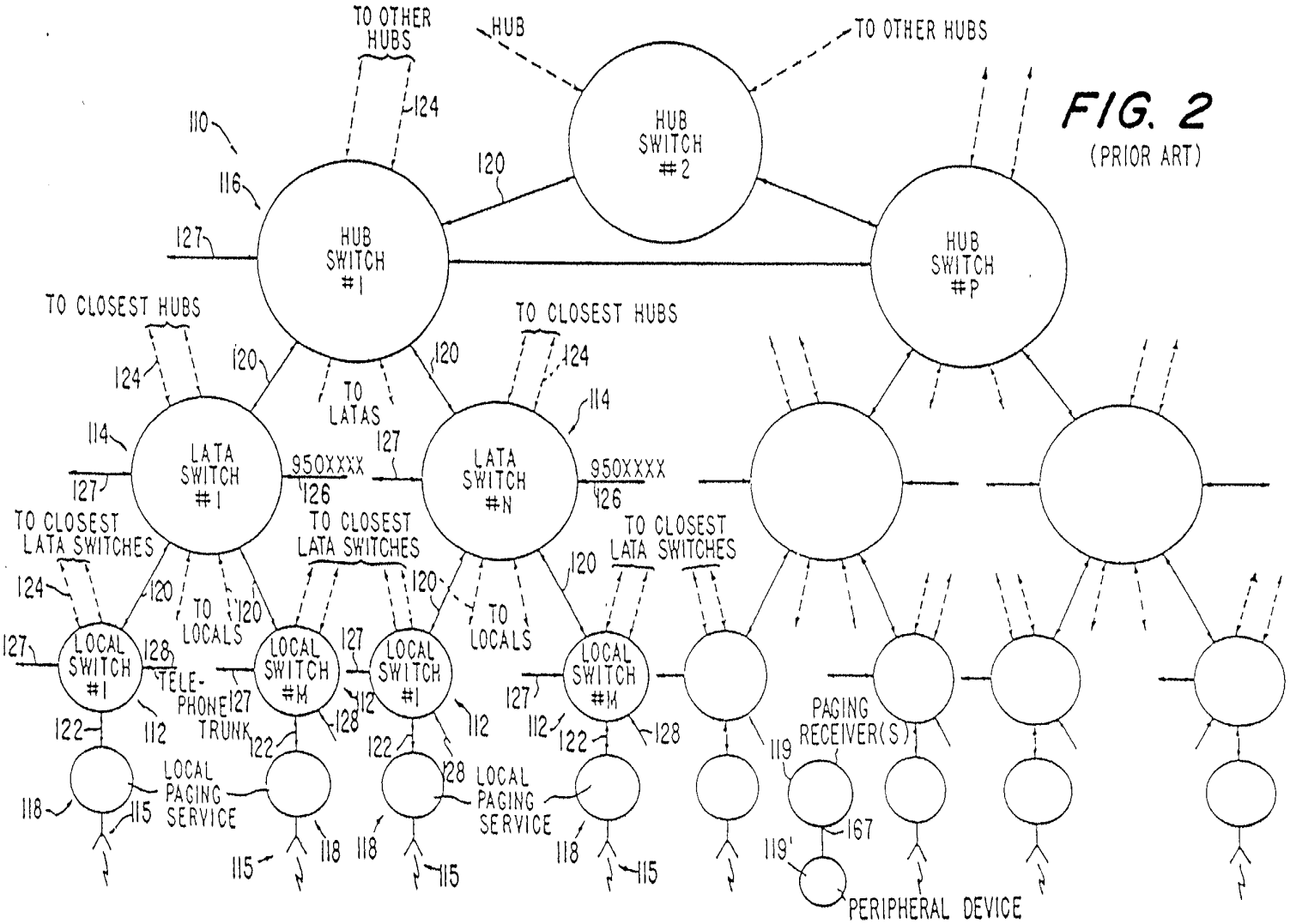


FIG. 2
(PRIOR ART)

FIG. 3
(PRIOR ART)

LOCAL SWITCH MEMORY MAP

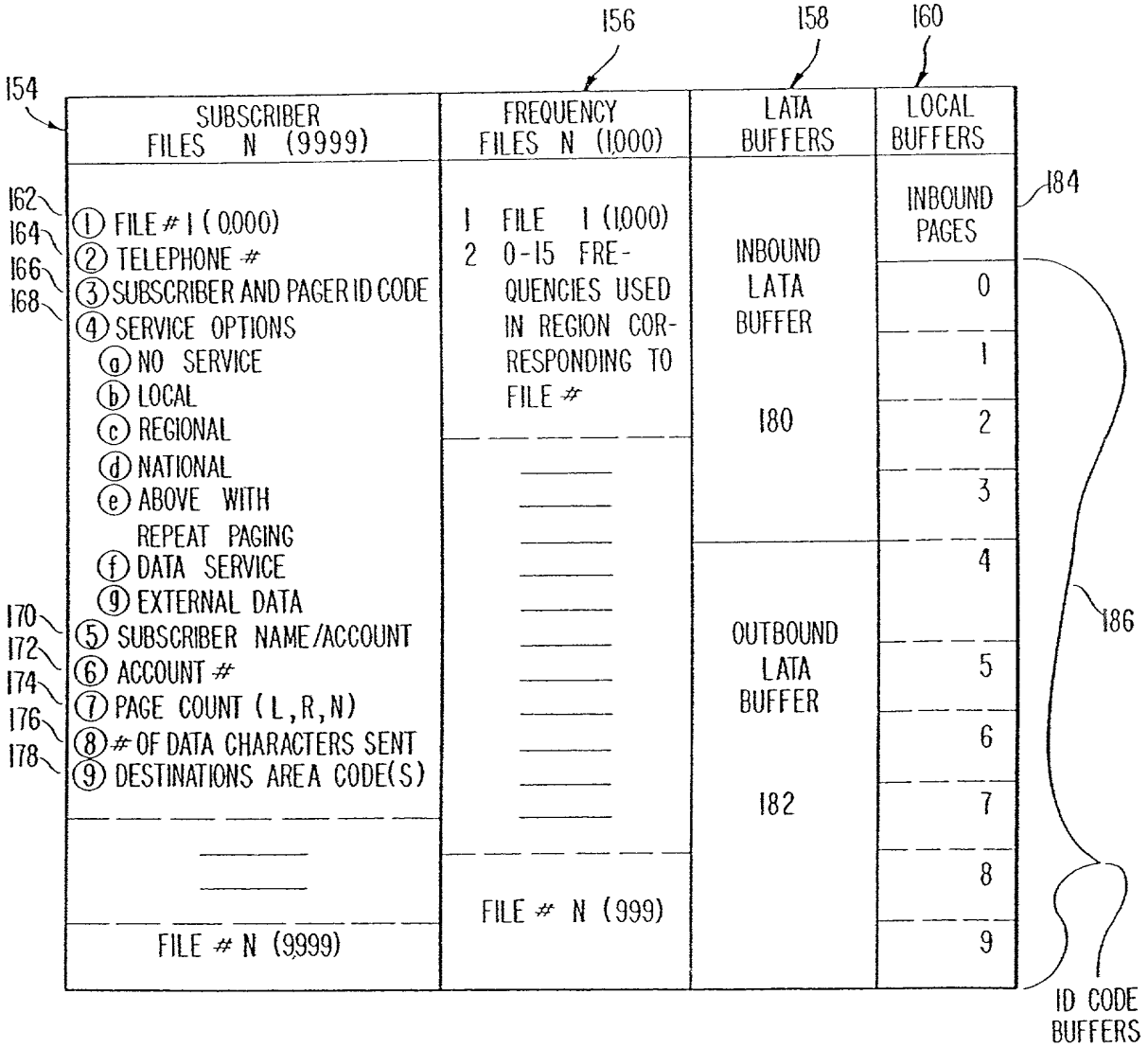


FIG. 4

(PRIOR ART)

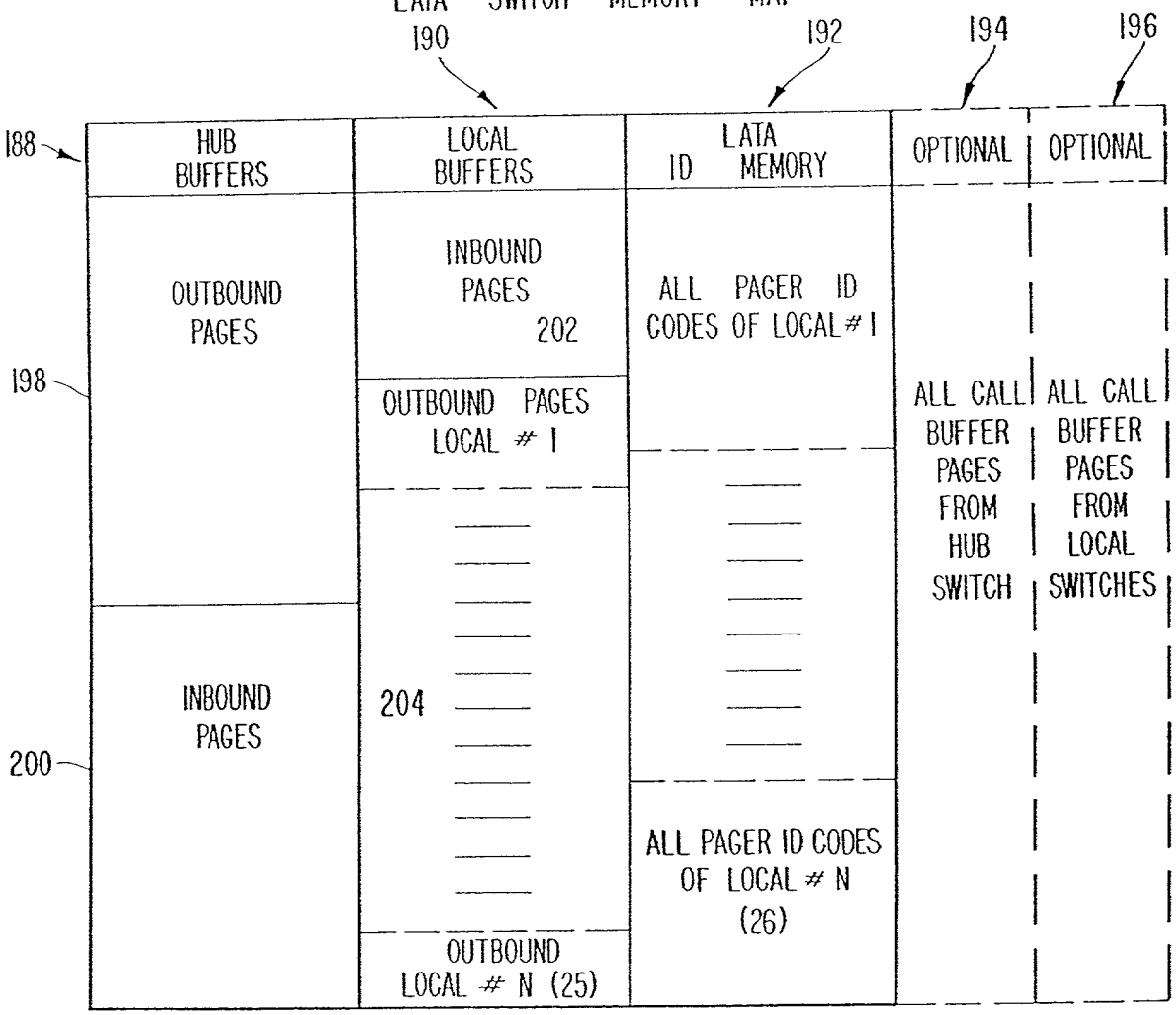


FIG. 5

(PRIOR ART)

HUB SWITCH MEMORY MAP

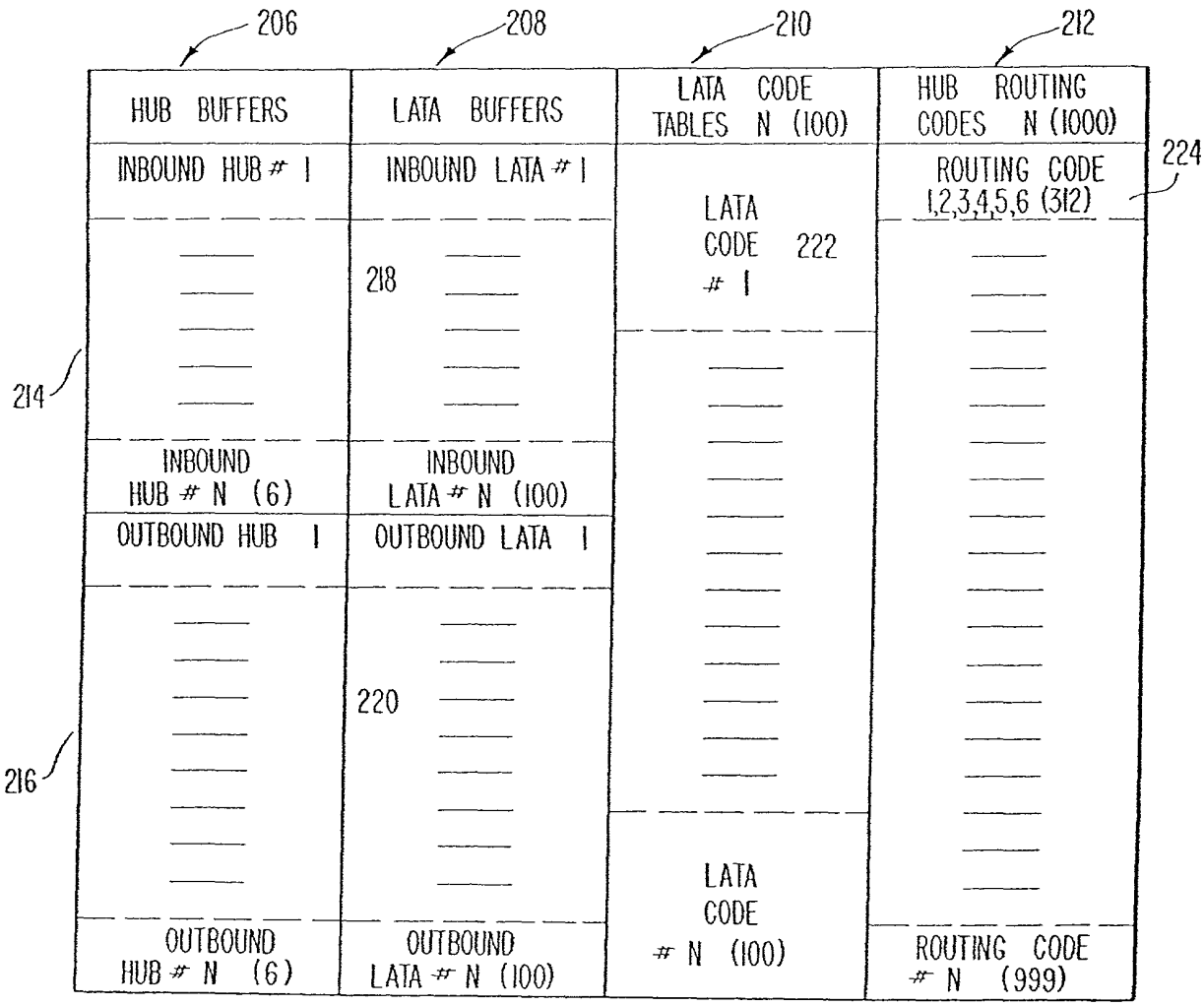


FIG. 6
PRIOR ART

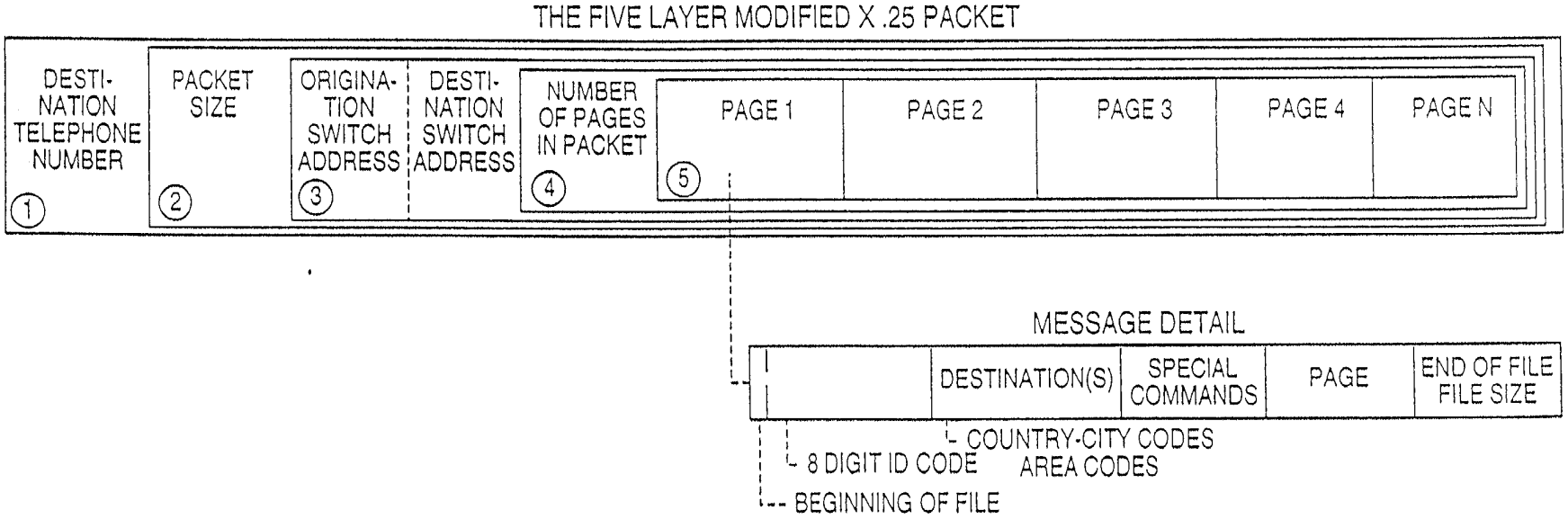
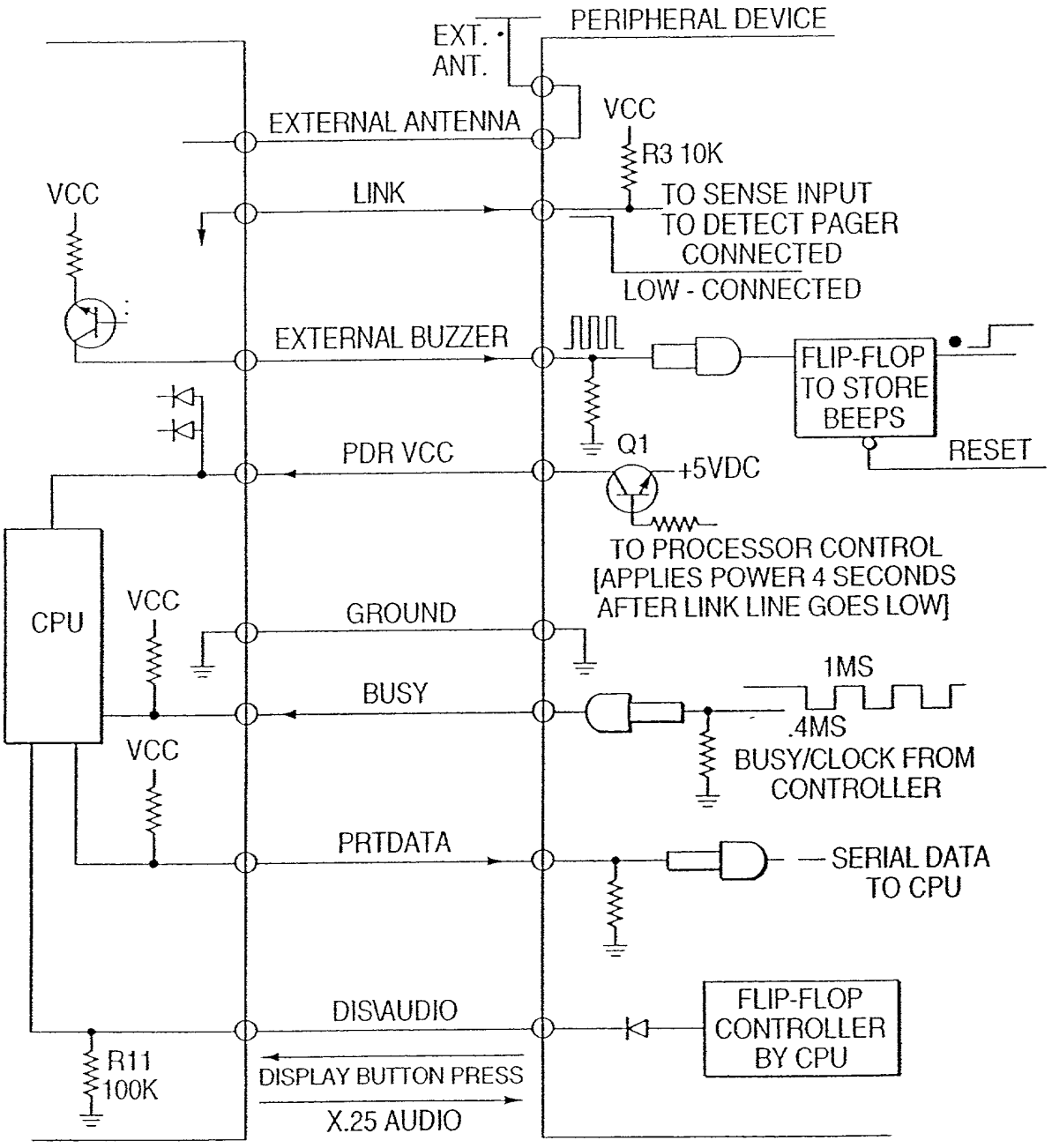
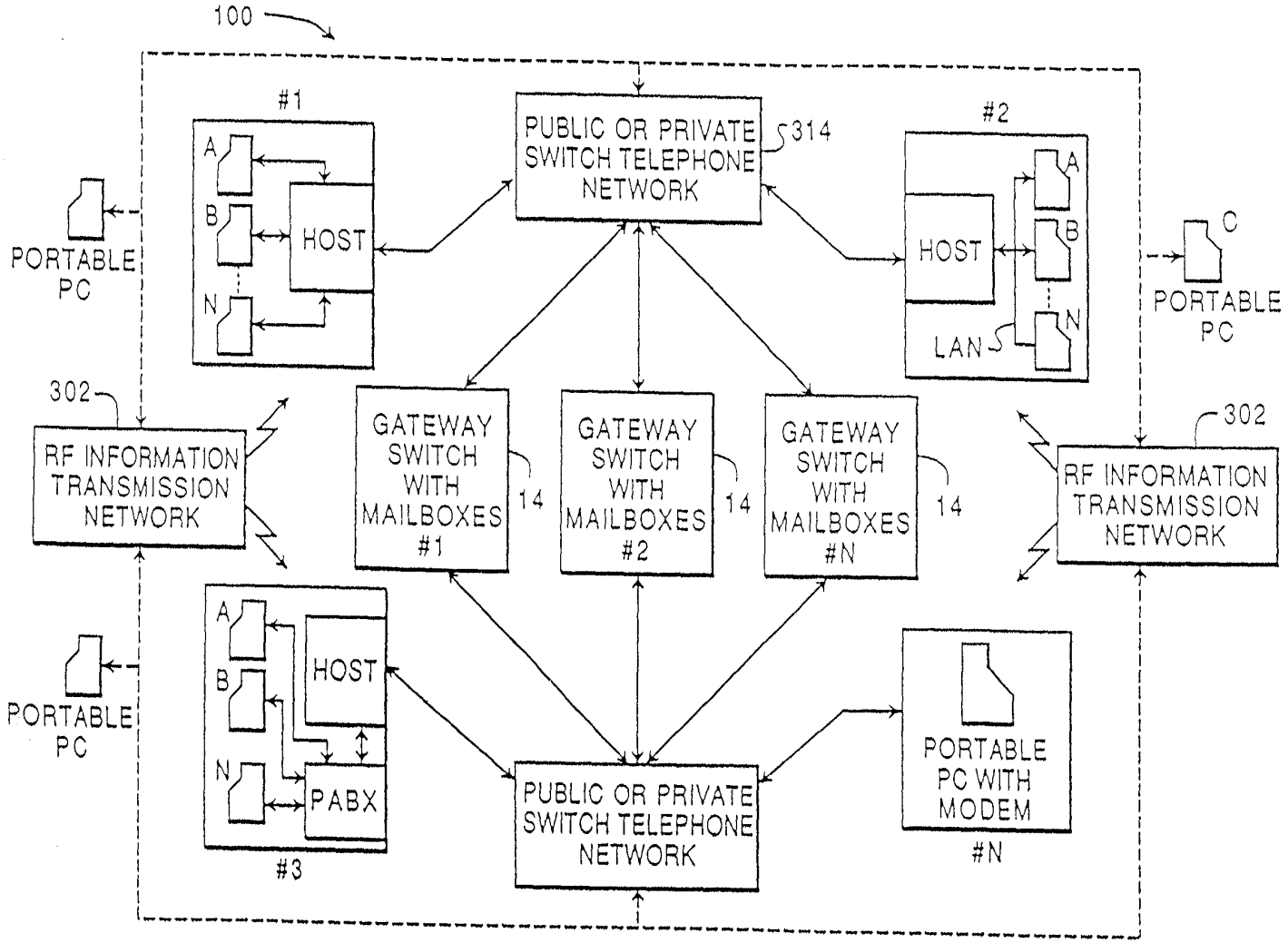


FIG. 7
PRIOR ART



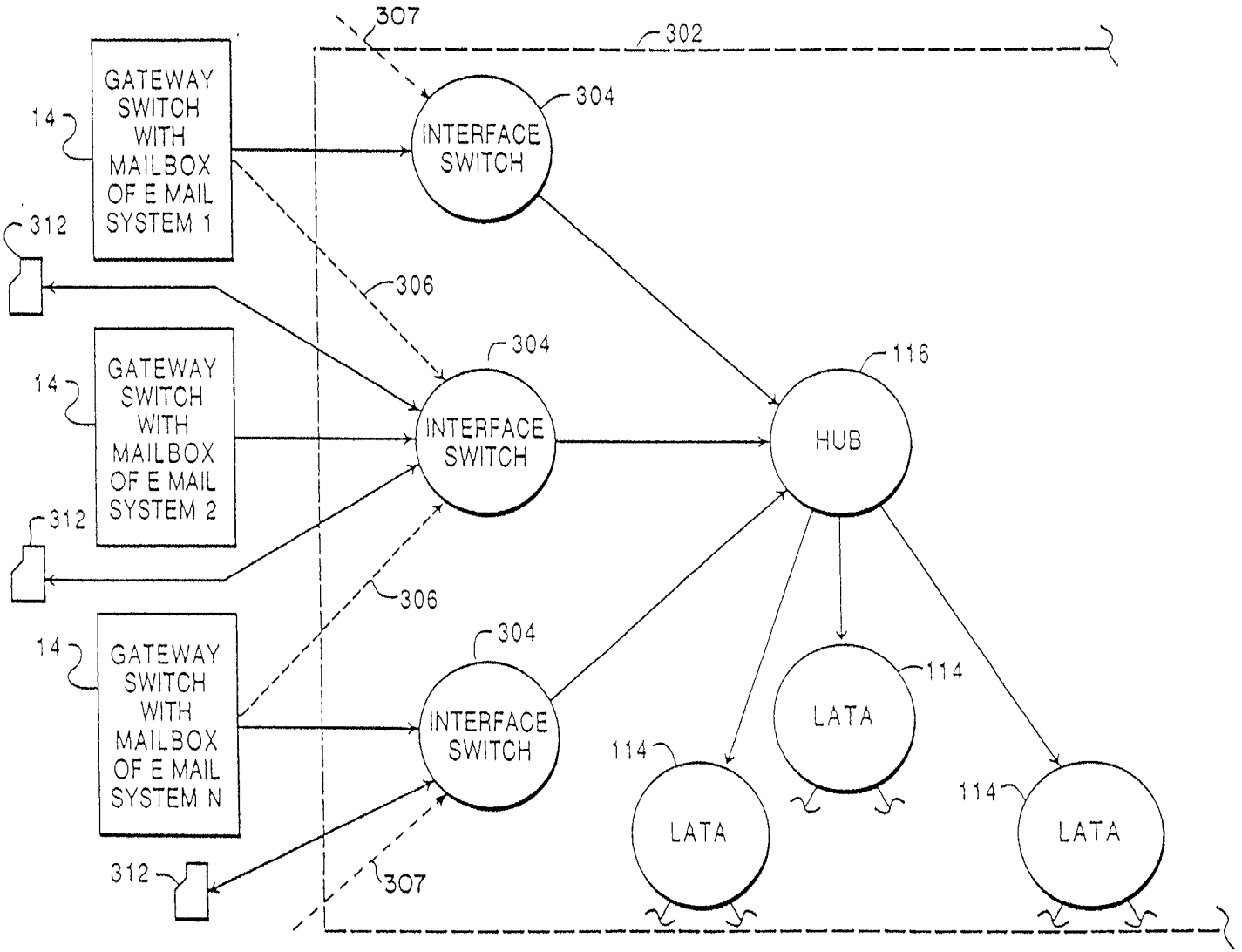
20h0E0" 9h89B00t

FIG. 8



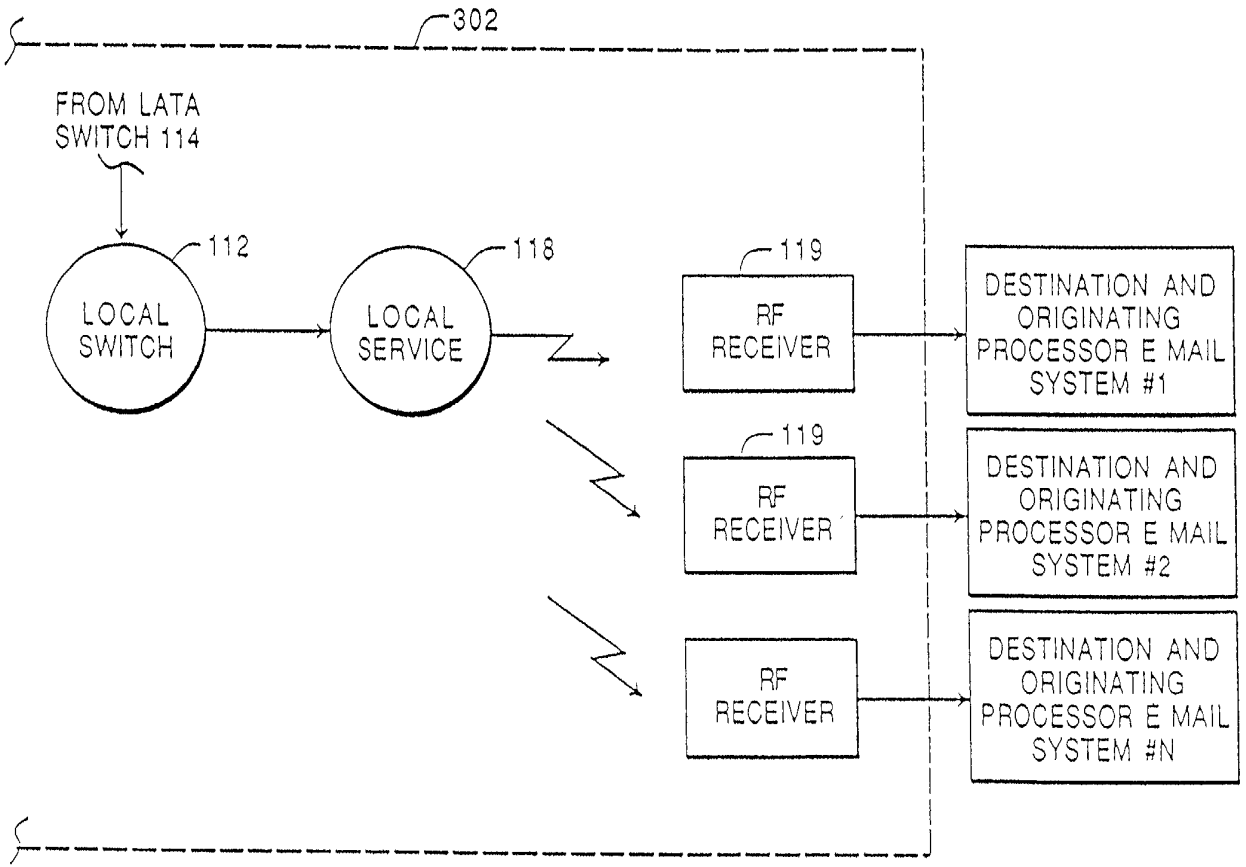
20h050 94898001

FIG. 9



30+0E0" 94:E9E80Ot

FIG. 10



ENTRY METHOD	ORIGINATING PROCESSOR →	GATEWAY SWITCH 14 →	INTERFACE SWITCH 304
1	ADDS INTERFACE (WIRELESS) DESTINATION AND DESTINATION PROCESSOR	NO-ACTION	ADDS ID OF RF RECEIVER 119
2	ADDS INTERFACE (WIRELESS) DESTINATION AND ID OF RECEIVER 119	NO-ACTION	NO ACTION OTHER THAN ID VERIFICATION
3	ADDS DESTINATION PROCESSOR	ADDS WIRELESS DESTINATION	ADDS ID OF RECEIVER 119
4	ADDS DESTINATION PROCESSOR	ADDS WIRELESS DESTINATION AND ID OF RECEIVER 119	NO ACTION OTHER THAN ID VERIFICATION
5	ADDS DESTINATION PROCESSOR, OPERATOR POINTS TO DISPLAYED ICON, ORIGINATING PROCESSOR ADDS WIRELESS DESTINATION.	ADDS ID OF RECEIVER 119	NO ACTION OTHER THAN ID VERIFICATION
6	ADDS DESTINATION PROCESSOR, OPERATOR POINTS TO DISPLAYED ICON, ORIGINATING PROCESSOR ADDS WIRELESS DESTINATION.	NO-ACTION	ADDS ID OF RECEIVER 119
7	ADDS DESTINATION PROCESSOR, OPERATOR POINTS TO DISPLAYED ICON, ORIGINATING PROCESSOR ADDS WIRELESS DESTINATION AND ID OF RECEIVER 119(BY COMPARING DESTINATION PROCESSOR TO ID TABLE).	NO-ACTION	NO ACTION OTHER THAN ID VERIFICATION

FIG. 11

30-10207 9k99800t

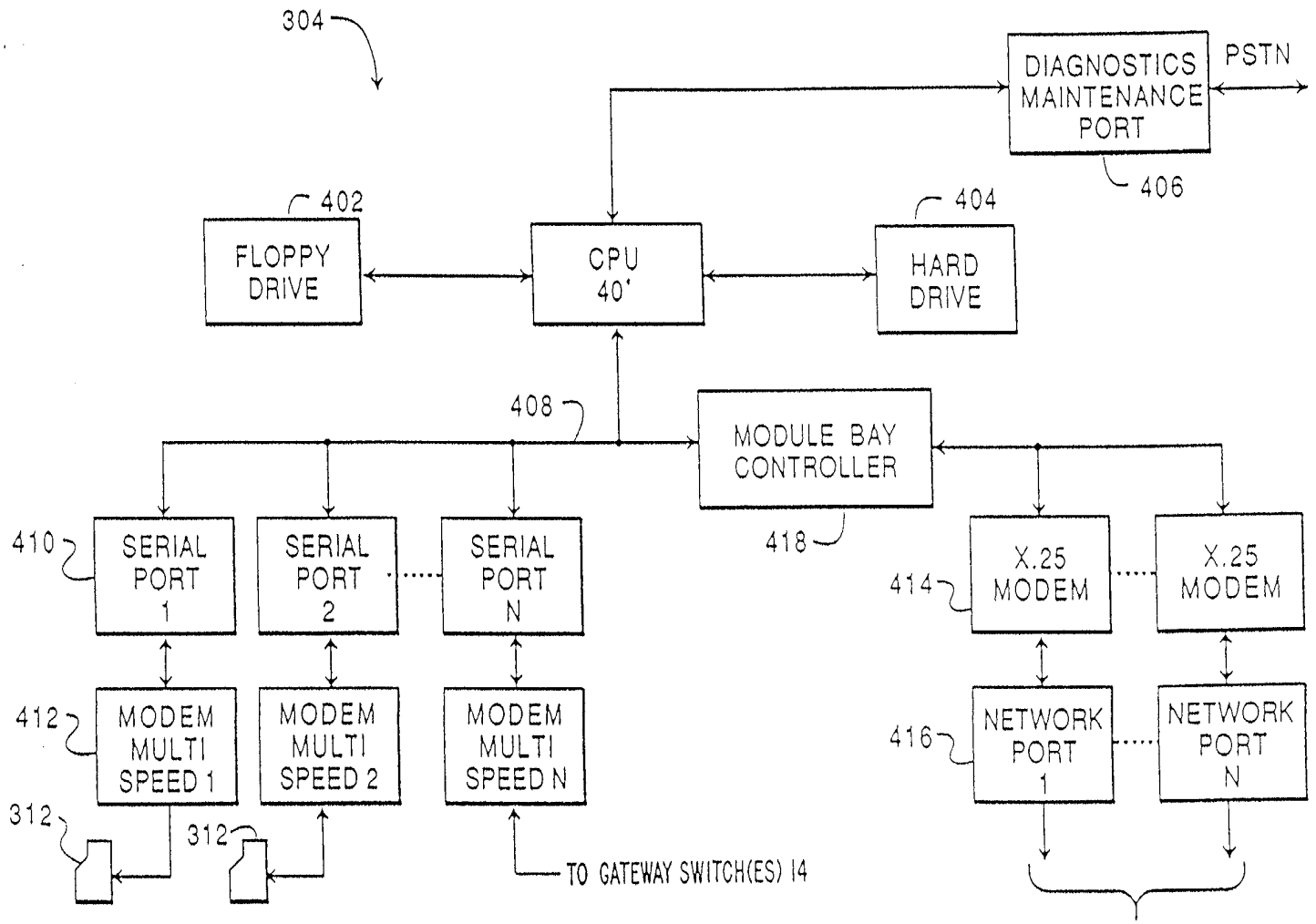



FIG. 12